

PRAKHAR GUPTA

Champaign, IL

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Education

University of Illinois Urbana Champaign

Aug. 2022 – May 2026

Bachelor of Science in Computer Engineering

Relevant Coursework

- Computer Architecture
- Operating Systems
- Distributed Systems
- Digital Design
- Digital Signal Processing
- Transistor Circuits
- Data Structures
- Engineering Stats
- Linear Algebra

Experience

National Center for Supercomputing Applications

Jun 2024 – Present

Intern *Urbana, IL*

- Worked with the SEAS group to deploy telemetry service to track executables and software library usage on HPC clusters
- Increased performance by 13x with in-memory log caching and aggregate file transmission on a parallel file system
- Enhanced reliability by implementing signal handlers for preemptive logging before job timeout. Integrated log retrieval from cache in the Slurm Epilog
- Assisted systems engineering team with on-site datacenter maintenance

Mobility and Fall Prevention Research Lab

Jan 2023 – May 2024

Undergraduate Research Assistant *Champaign, IL*

- Deployed scientific computing pipeline. Performed code profiling and improved performance by 25% via parallelization (cupy, numba, and cython). Developed vector-based analyses for studying network dynamics in the brain
- Automated environment and data management with bash scripts. Wrote acquisition/ingestion scripts for large datasets
- Developed, tested, and assembled custom wireless sensing devices for clinical studies

ECE Department, University of Illinois

Jan 2024 – Present

Undergraduate Teaching Assistant *Champaign, IL*

- Analog Signal Processing: Organized weekly lectures, created assignment outlines and final project base design
- Operating Systems: Developed auto grader scripts assignments, Helped debug development issues during office hours

Indian Institute of Technology (IIT)

Jun 2023 – Aug 2023

Full-Stack Intern *Mumbai, India*

- Developed front-end user systems and an authorization microservice for an internal platform
- Implemented end-to-end services within an MVC architecture using Express, MongoDB, and other full-stack toolkits
- Wrote and tested API endpoints with Postman, and implemented a real-time pub-sub service with websockets

Projects

Linux Kernel | *C, x86* : Developed a kernel from scratch for a single-core x86 system. Implemented hardware drivers, paging, interrupt support, filesystem, syscalls, and concurrency through a round-robin scheduler. Implemented UART PvP TicTacToe and Soundblasters (3rd place in design competition)

Distributed Data Processing Program | *Go* : Developed in Go using RPCs, capable of querying, filtering, and processing distributed files akin to Hadoop. Implemented subroutines for maintaining membership in the network and handling failures

RV32i CPU | *SystemVerilog, Synopsis* : Designed & synthesized a 5-staged pipelined RV32I CPU in SystemVerilog.

Safeguarded against hazards with datapath forwarding and branch prediction. Verified with randomized constrained tests

DSP Harness | *C, FreeRTOS, ARM CMSIS* : Created a DSP harness on a dual-core cortex M0+ system to support digital filters via user-provided function references. Configured DMA, ADC, I2S codec, integrated FreeRTOS, and the ARM CMSIS-DSP library

Wireless Sensor | *ESP-IDF, MQTT, KiCad* : Designed an electronic sensor for a Tribo-Electric Nano Generator sensor. Used op-amp input buffer and ADC for signal acquisition. Implementing wireless services like dynamic pairing and real-time data-logging. Custom board bringup in KiCad

Technical Skills

Languages: C, Python, C++, Assembly, SystemVerilog, Go

General: Linux, Git, Bash, Docker, CMake

Hardware: KiCad, Synopsis VCS/DV, Xilinx Vivados, FreeRTOS

Extracurricular Involvement

Open-Source at Illinois

Aug 2022 – May 2024

President

- Organized workshops, events, and activities to popularize FOSS usage and contributions. Managed project teams for EOH to develop projects related to CV and LLMs